

FEATURES

- 16 mΩ High-Side MOSFET in SOT23-6
- 2.0~4.0 A Adjustable Current Limit
- Low Average Current in OUT shorted GND
- Support Apple @ 2.4A fast Charging
- Support Samsung @ 2.1A fast Charging
- Support BC1.2 & YD/T 1691-2009 Charging
- Built-in Soft-Start
- Available SOT23-6 package

APPLICATIONS

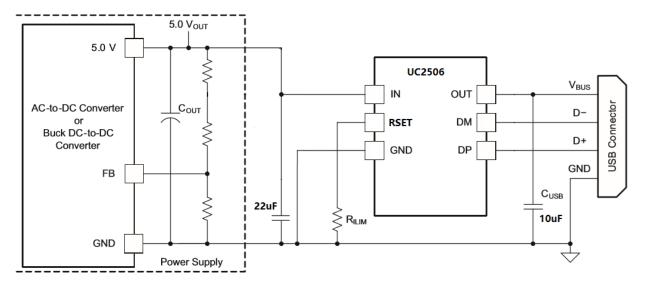
- USB Charger
- USB Wall Adapter
- Car Charger

DESCRIPTION

The UC2506 integrated USB charger emulators with automatic host charger identification circuitry and high performance adjustable current limiting power switch. An automatic USB charger identification circuit allows mobile power supply can automatically provides the correct modes on the data lines to charger compliant devices among the Apple, Samsung and BC1.2 modes.

The UC2506 is a $16m\Omega$ in SOT23-6 package power switch intended for applications where heavy capacitive loads and short-circuits are likely to be encountered. This also provides hiccup mode when OUT voltage is less than 3.0V or OTSD.

PACKAGE AND APPLICATION

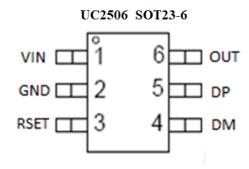


ORDING INFORMATION

Part Number	Package Type	Package Qty	Op Temp(°C)	Mark
UC2506	SOT23-6	3000	-40~85	UC2506 XXX



PINOUT

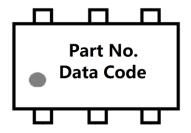


PIN FUNCTIONS

Dia Massa	TYP	E(1)	DESCRIPTION		
Pin Name	SOT23-6		DESCRIPTION		
VIN	1	P/I	Power supply/Input voltage connected to Power Switch; connect a 10 µF or greater ceramic capacitor from IN to GND as close to the IC as possible		
GND	2	G	Ground connection		
RSET	3	I	External resistor used to set current-limit threshold;		
DM	4	I	DM data line to connector, input for hand-shake voltage from portable equipment high impedance while disabled		
DP	5	I	DP data line to connector, input for hand-shake voltage from portable equipment high impedance while disabled		
OUT	6	О	Power-switch output, connected to VBUS of USB; connect a 10 µF or greater ceramic capacitor from OUT to GND as close to the IC as possible		

(1) G = Ground, I = Input, O = Output, P = Power

MARK INFORAMTION



注: UC2506, AXXX, 第一个X指年份, 后面两个X指哪周



ABSOLUTE MAXIMUM RATINGS (1)

Over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER			MAX	UNIT	
Supply Voltage Range	IN	-0.3	7.0	V	
Input voltage range	DP, DM	-0.3	5.8	V	
Continuous output sink current	DP input current, DM input current		35	A	
Continuous output source current	DP output current, DM output current		35	mA	
ESD rating, Human Body Model (HBM)	IN, DP, DM		6	kV	
Operating Junction Temperature	$T_{\rm J}$	-40	125	0.0	
Storage Temperature Range	$T_{ m stg}$	-65	160	°C	

⁽¹⁾ Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

THERMAL CHARACTERISTICS

over operating free-air temperature range (unless otherwise noted)

	UNIT		
$\theta_{ m JA}$	ESOP8 Package thermal impedance ⁽¹⁾	45	
$\theta_{ m JA}$	EMSOP8 Package thermal impedance ⁽¹⁾	65	°C/W
$\theta_{ m JA}$	SOT23-6 Package thermal impedance ⁽¹⁾	165	

⁽¹⁾ The package thermal impedance is calculated in accordance with JESD 51-7.

RECOMMENDED OPERATING CONDITIONS

	MIN	MAX	UNIT		
V _{IN}	Input voltage of IN	4.5	6.5	V	
V _{DP/DM}	DP data line input voltage		5.5		
I _{DP/DM}	Continuous sink/source current		±10	mA	
R_{SET}	Resistance of R _{SET}	13	100	kΩ	
I_{OUT}	Continuous sink/source current	2000	4000	mA	
T _J	Operating Junction Temperature	-40	125	°C	

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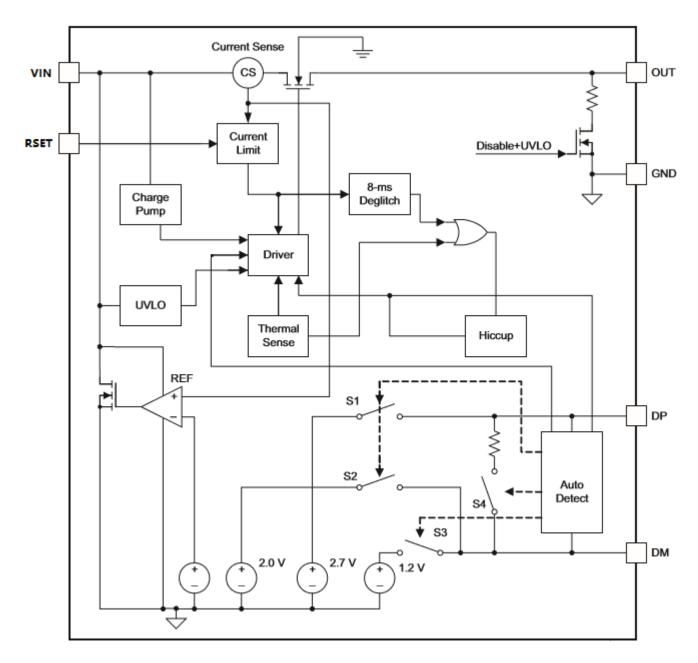
ELECTRICAL CHARACTERISTICS

Conditions are: TA = 25 C, IN = 5.0 V, Positive current are into pins. All voltages are with respect to GND (unless otherwise noted).

PARAMETER		TEST CONDITIONS	MIN	ТҮР	MAX	UNIT		
	Power Switch							
RDSON	SOT23-6	IOUT=2.4A		16		mΩ		
		Current Limit						
Iout	OUT current limited	BIN1:RSET=19.1k	2.5	2.75	3.0	A		
	IF	PAD MODE 2.4A Mode						
V _{DP_IPAD}	DP output voltage		2.5	2.7	2.9	1 7		
$V_{\text{DM_IPAD}}$	DM output voltage		2.5	2.7	2.9	V		
	•	Galaxy Tab MODE						
V_{DP_GAL}	DP output voltage		1.1	1.2	1.3	V		
$V_{\text{DM_GAL}}$	DM output voltage		1.1	1.2	1.3	V		
		SUPPLY CURRENT						
$I_{\rm IN}$	IN supply current	IN= 5.0V,		230	400			
I_{INL}	IN Disable Supply Current	IN= 5.0V		0	5	μА		
		Thermal Shutdown						
Totsd	Temperature Rising Threshold			160		С		
T_{HYS}	Hysteresis			20				



FUNCTION BLOCK DIAGRAM





PCB LAYOUT NOTIFICATION

Input capacitance CIN(red position) of the pin 1 of uc2506:

The voltage entering the pin 1 must pass through the input capacitor CIN at a single point(单点过电容), the CIN must be close to the pin1.

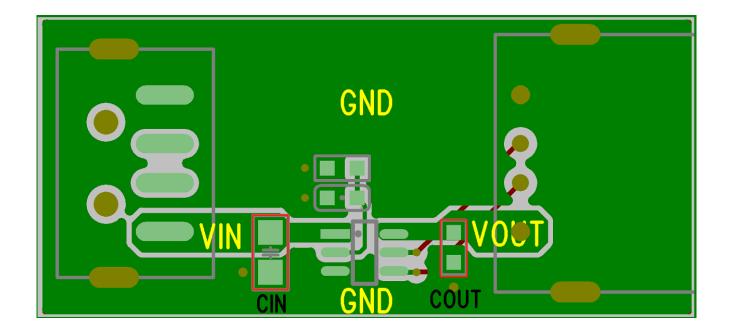
Output capacitance COUT(red position)of pin 6:

The pin6 to USB must pass through the output capacitor COUT and be close to pin 6.

Recommended capacitance CIN is 0805 size, value is 22uF;

Recommended capacitance COUT is 0805 size, value is 10uF;

Based the difference of AC/DC or DC/DC, the CIN and COUT can bigger or smaller.





PACKAGE INFORMATION

SOT23-6

